

International Solid-State Circuits Conference 2018 Reports Program (2018/2/27)

Session Chair	Time	Title	Speaker(Affiliation)
Dr. Tetsuya Iizuka (University of Tokyo)	10:00 10:05	Opening	<u>Makoto Ikeda</u> (University of Tokyo)
	10:05 10:20	Review of ISSCC 2018	<u>Makoto Takamiya</u> (University of Tokyo)
	10:20 10:45	A 20ns-Write 45ns-Read and 1014-Cycle Endurance Memory Module Composed of 60nm Crystalline Oxide Semiconductor Transistors	<u>Yuto Yakubo</u> (Semiconductor Energy Laboratory)
	10:45 11:10	A 512Gb 3b/Cell 3D Flash Memory on a 96-Word-Line-Layer Technology	<u>Hiroshi Maejima</u> (Toshiba Memory)
	11:10 11:35	QUEST: A 7.49TOPS Multi-Purpose Log-Quantized DNN Inference Engine Stacked on 96MB 3D SRAM Using Inductive-Coupling Technology in 40nm CMOS	<u>Kodai Ueyoshi</u> (Hokkaido University)
	11:35 12:00	A 286F2/Cell Distributed Bulk-Current Sensor and Secure Flush Code Eraser Against Laser Fault Injection Attack	<u>Kohei Matsuda</u> (Kobe University)
	12:00 13:30	Lunch Break	
Dr. Toshiya Mitomo (Toshiba)	13:30 14:10	Future Mobile Society Enabled by Semiconductor Technology	<u>Yoshihiko Isobe</u> (DENSO)
	14:10 14:35	A Back-Illuminated Global-Shutter CMOS Image Sensor with Pixel-Parallel 14b Subthreshold ADC	<u>Masaki Sakakibara</u> (Sony Semiconductor Solutions)
	14:35 15:00	A 1/4-inch 3.9Mpixel Low-Power Event-Driven Back-Illuminated Stacked CMOS Image Sensor	<u>Oichi Kumagai</u> (Sony Semiconductor Solutions)
	15:00 15:25	A 20ch TDC/ADC Hybrid SoC for 240×96-Pixel 10%-Reflection <0.125%-Precision 200m-Range Imaging LiDAR with Smart Accumulation Technique	<u>Kentaro Yoshioka</u> (Toshiba)
	15:25 15:50	A 2.1μm 33Mpixel CMOS Imager with Multi-Functional 3-Stage Pipeline ADC for 480fps High-Speed Mode and 120fps Low-Noise Mode	<u>Shoji Kawahito</u> (Shizuoka University)
	15:50 16:15	Break	
Dr. Ryuichi Fujimoto (Toshiba Memory)	16:15 16:40	An 802.11ax 4×4 Spectrum-Efficient WLAN AP Transceiver SoC Supporting 1024QAM with Frequency-Dependent IQ Calibration and Integrated Interference Analyzer	<u>Toshiya Mitomo</u> (Toshiba)
	16:40 17:05	A 120Gb/s 16QAM CMOS Millimeter-Wave Wireless Transceiver	<u>Korkut K. Tokgoz</u> (Tokyo Institute of Technology)
	17:05 17:30	A 0.98mW Fractional-N ADPLL Using 10b Isolated Constant-Slope DTC with FOM of -246dB for IoT Applications in 65nm CMOS	<u>Hanli Liu</u> (Tokyo Institute of Technology)
	17:30 17:55	An ADPLL-Centric Bluetooth Low-Energy Transceiver with 2.3mW Interference-Tolerant Hybrid-Loop Receiver and 2.9mW Single-Point Polar Transmitter in 65nm CMOS	<u>Hanli Liu</u> (Tokyo Institute of Technology)
	17:55 18:20	Food and Agriculture Cloud Services with Sensor Networks	<u>K. Watanabe</u> (Fujitsu Kyushu System Services)

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