

## **2024 Activities of the IEEE Circuits and Systems Society Japan Joint Chapter (CASS JJC)**

The IEEE Circuits and Systems Society Japan Joint Chapter (CASS JJC) consists of six sections: Tokyo, Sapporo, Sendai, Nagoya, Shin-Etsu, and Hiroshima. Each year, we support approximately ten technical meetings and events related to circuits and systems, in collaboration with domestic organizations such as IEICE Japan.

A key activity in 2024 was the Taiwan and Japan Conference on Circuits and Systems (TJCAS) held in Kumamoto, Japan, which attracted approximately 100 participants, including 50 students, with about half coming from each country. TJCAS is an international conference that alternates between Taiwan and Japan, focusing on research in circuits and systems. This year, in conjunction with TJCAS, CASS JJC also organized a Young Professionals (YP)/Industry Meeting as part of a CASS outreach initiative to promote exchanges among young researchers and students.

Kumamoto, a significant hub for the semiconductor industry, has experienced increased international engagement following TSMC's arrival. The event featured keynote lectures on semiconductor education and trends in LSI design, as well as discussions on education, research, and international collaboration.

The outreach event featured active discussions on topics such as "What makes semiconductor research appealing?" and "How can we educate students to foster interest in circuits and systems?" These interactions highlighted the vibrant exchange fostered through semiconductor research.

Hosting these events in Kumamoto supported the professional development of young researchers and students, aligning with CASS's goals to encourage career growth.

